



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#5/election  
10/8/02  
Jame H

In re the Application of:

Suzuki et al.

Art Unit: 2811

Application No.: 10/058,005

Examiner: Nadav, Ori

Filed: January 29, 2002

Attorney Dkt. No.: 100353-00096

For: SEMICONDUCTOR INTEGRATED CIRCUIT CONFIGURED TO SUPPLY  
SUFFICIENT INTERNAL CURRENT

**RESPONSE TO RESTRICTION REQUIREMENT**

Commissioner for Patents  
Washington, D.C. 20231

September 26, 2002

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TECHNOLOGY CENTER 2800

Sir:

The Office Action dated September 3, 2002 has been received and carefully noted. The following remarks are submitted as a full and complete response thereto.

The Office Action of September 3, 2002 was a restriction requirement, requiring that the Applicant elect to prosecute one of two inventions which were indicated as being recited in the application. The two inventions are as follows:

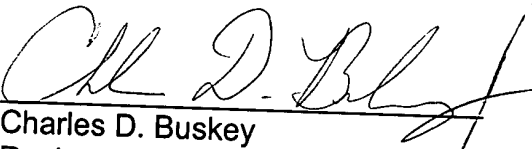
Invention I, recited in claims 1-3 and 10 drawn to a semiconductor device; and

Invention II, recited in claims 4-9, drawn to a process of making a semiconductor device.

Applicants respectfully elect, without traverse, to prosecute the subject matter of Invention I, recited in claims 1-3 and 10 drawn to a semiconductor device. Applicants reserve the right to file a divisional application on the non-elected subject matter. Timely examination on the merits is respectfully requested.

In the event this paper is not being timely filed, the applicants respectfully petition for an appropriate extension of time. Any fees for such an extension together with any additional fees may be charged to Counsel's Deposit Account 01-2300 making reference to Attorney Docket Number 100353-00096.

Respectfully submitted,

  
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